

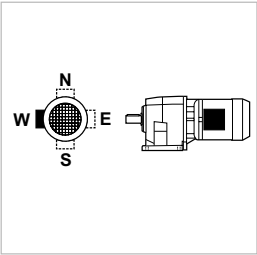
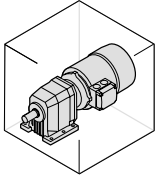
Input:

**HS**

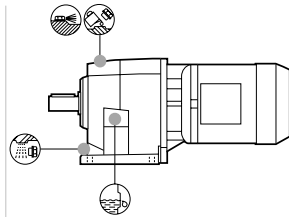
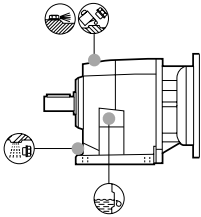
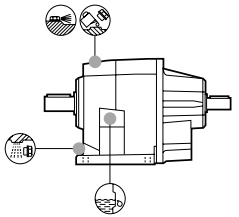
**P (IEC)  
N (NEMA)**

**S**

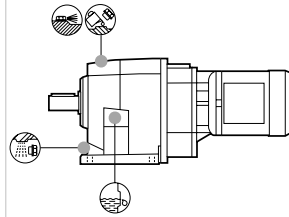
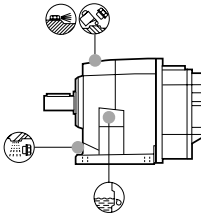
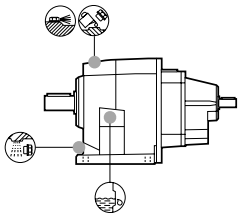
**B3**



W = Default

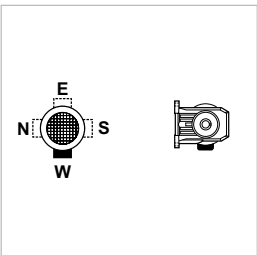
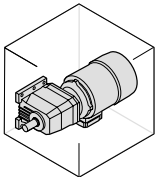


2x  
3x

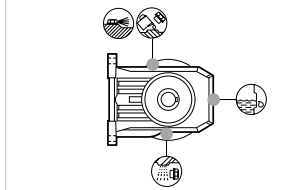
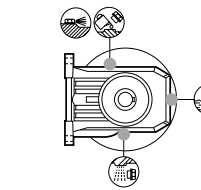
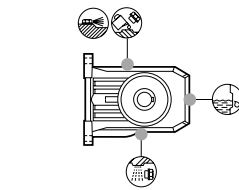


4x

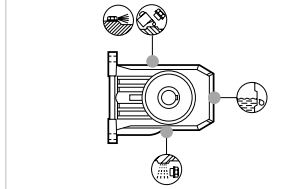
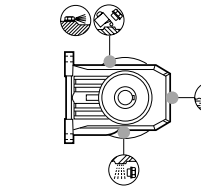
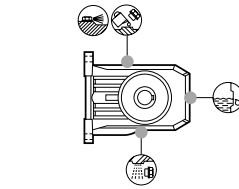
**B6**



W = Default

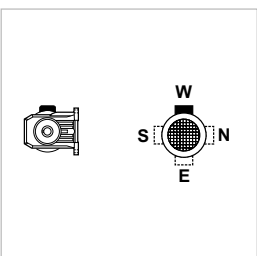
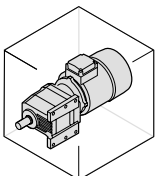


2x  
3x

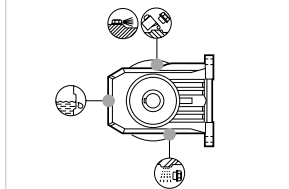
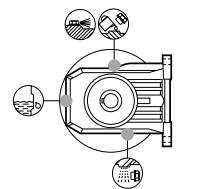
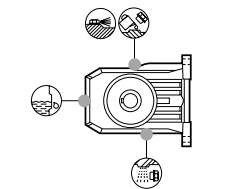


4x

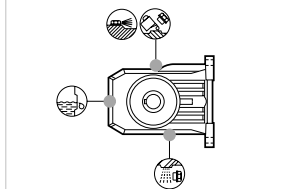
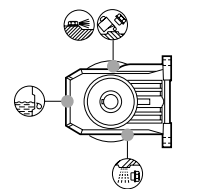
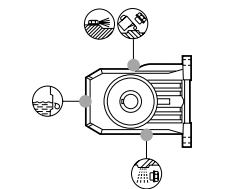
**B7**



W = Default



2x  
3x



4x

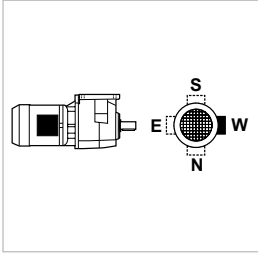
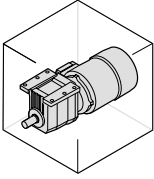
Input:

HS

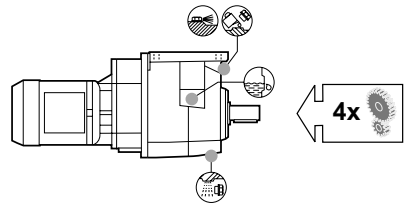
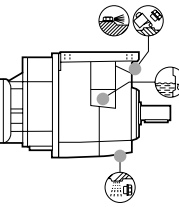
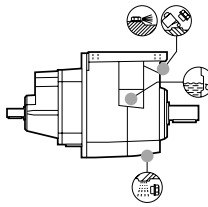
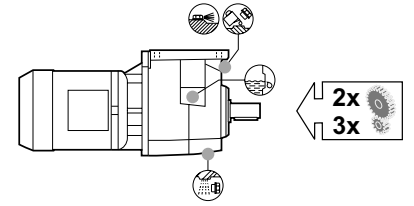
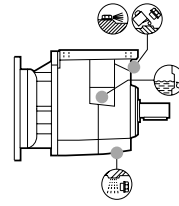
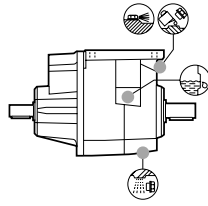
P (IEC)  
N (NEMA)

S

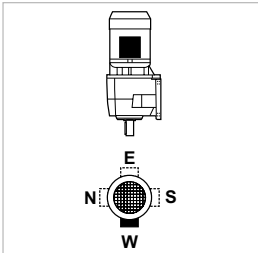
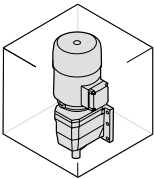
**B8**



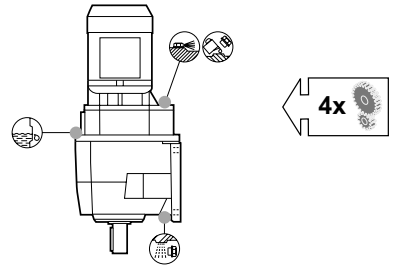
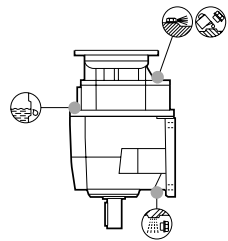
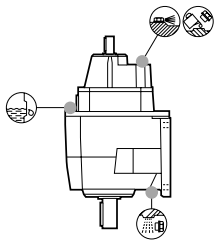
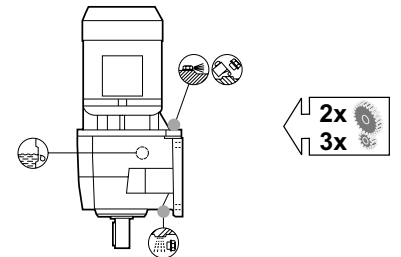
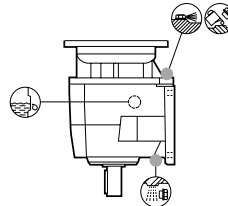
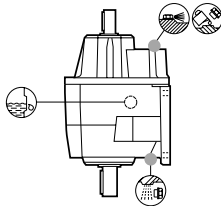
W = Default



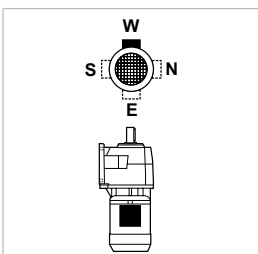
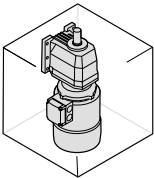
**V5**



W = Default



**V6**



W = Default

