

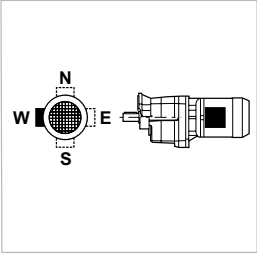
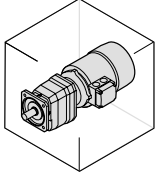
Input:

HS

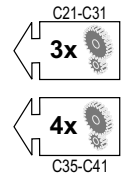
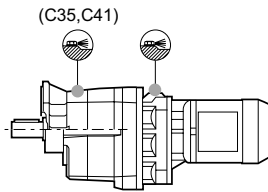
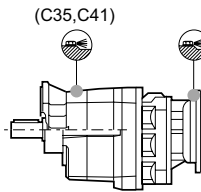
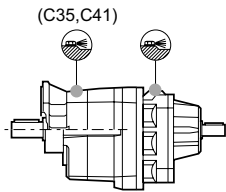
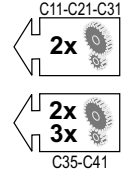
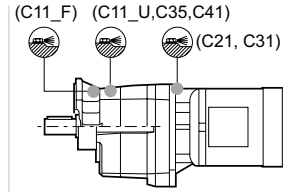
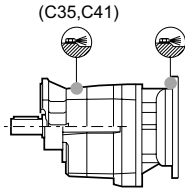
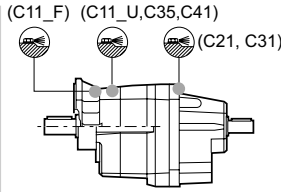
**P (IEC)
N (NEMA)**

S

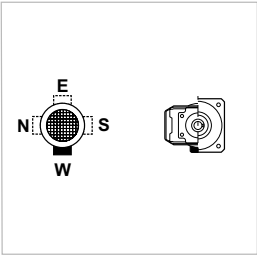
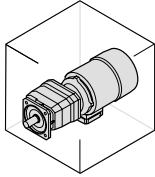
B5



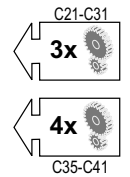
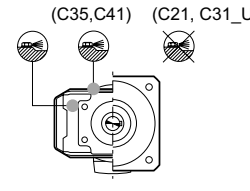
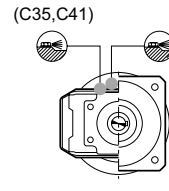
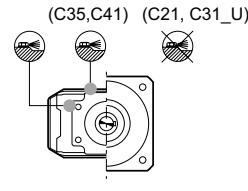
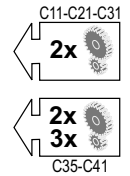
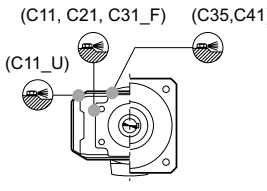
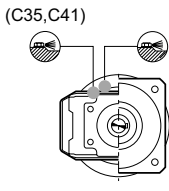
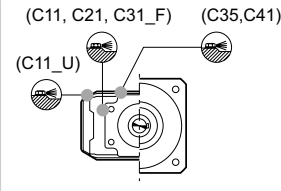
W = Default



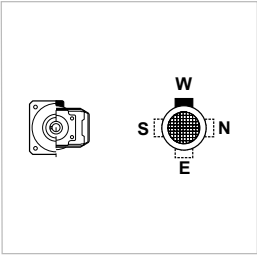
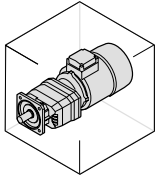
B51



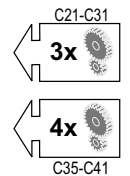
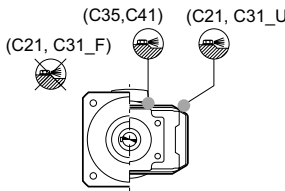
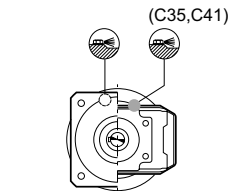
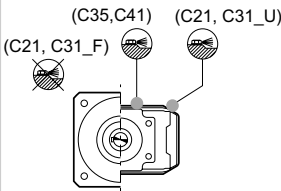
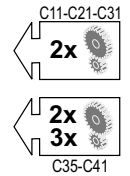
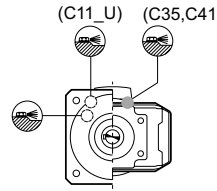
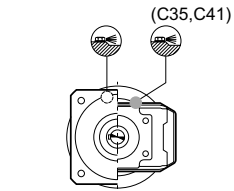
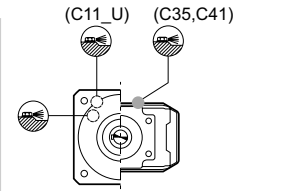
W = Default



B53



W = Default



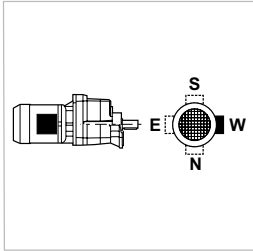
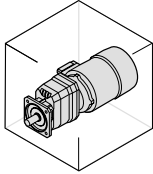
Input:

HS

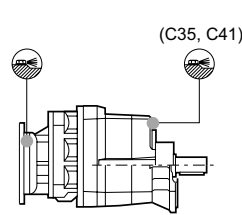
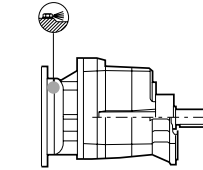
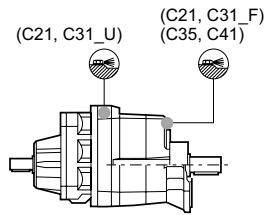
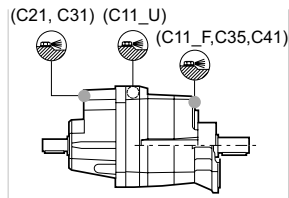
**P (IEC)
N (NEMA)**

S

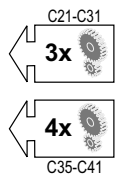
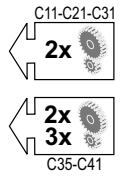
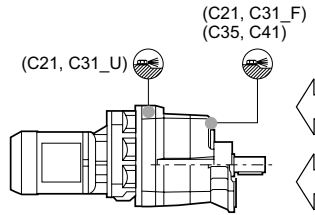
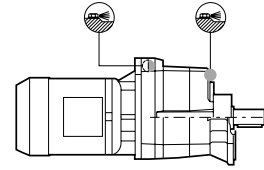
B52



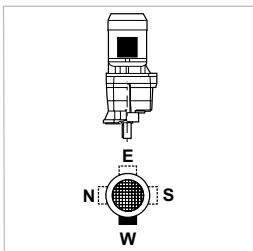
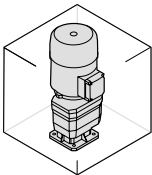
W = Default



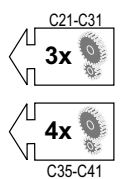
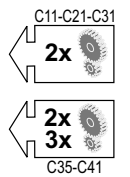
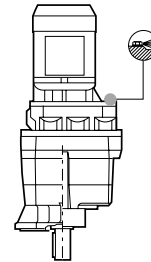
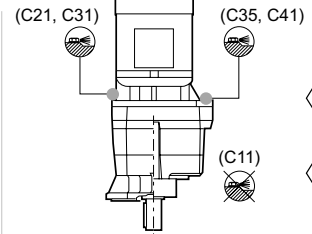
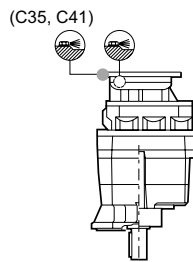
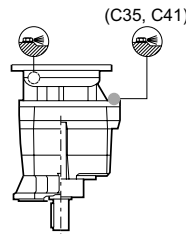
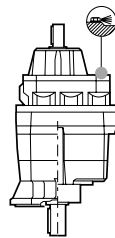
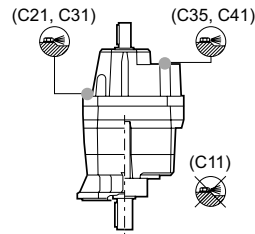
(C11_U) (C11_F)
(C21, C31_U) (C21, C31_F, C35, C41)



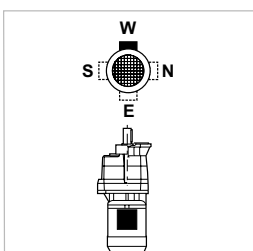
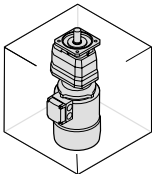
V1



W = Default



V3



W = Default

