

C 51...C 61

C_P

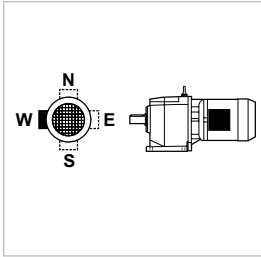
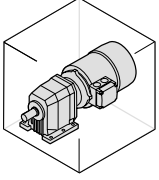
(B9)

_HS

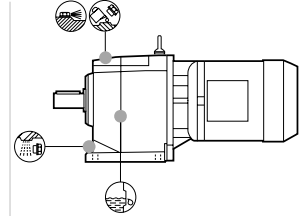
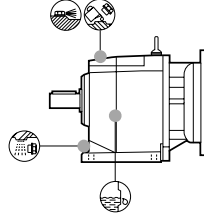
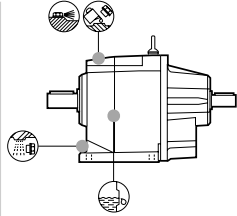
_P (IEC)

_S

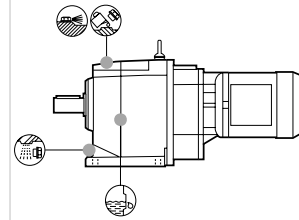
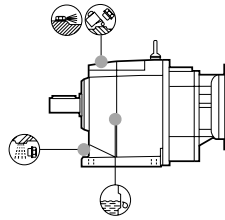
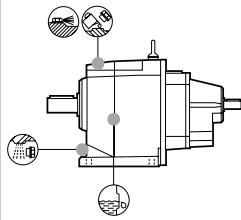
B3



W = Default

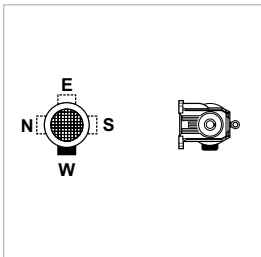
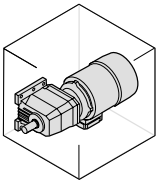


2x
3x

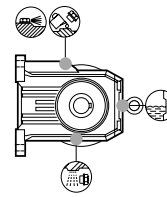
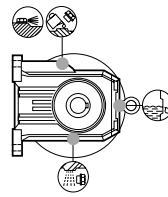
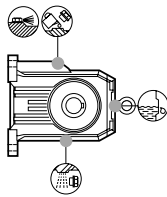


4x

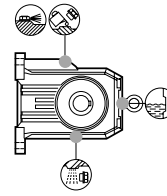
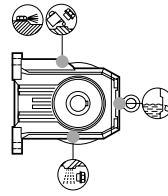
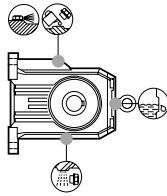
B6



W = Default

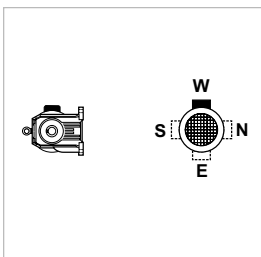
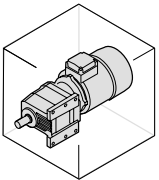


2x
3x

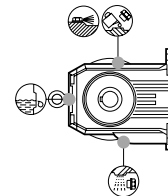
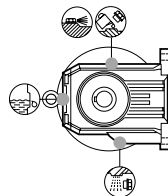
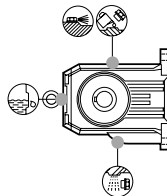


4x

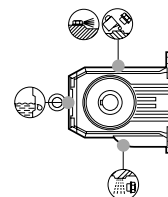
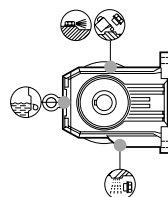
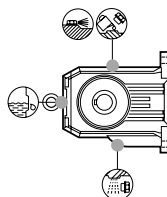
B7



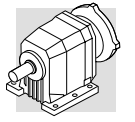
W = Default



2x
3x



4x



C_P

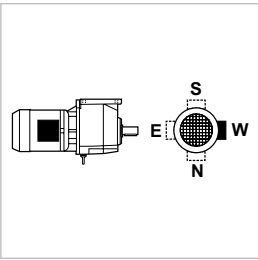
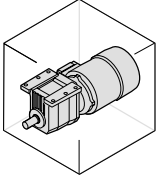
(B10)

_HS

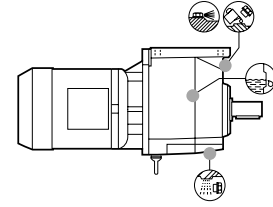
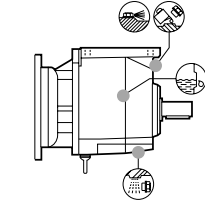
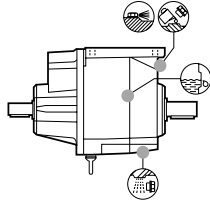
_P (IEC)

_S

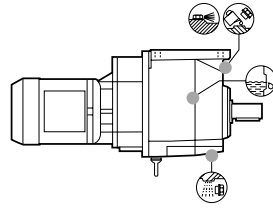
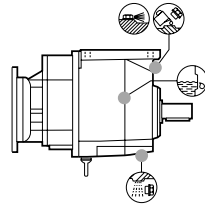
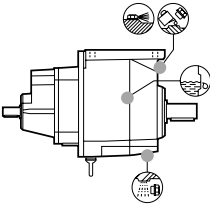
B8



W = Default

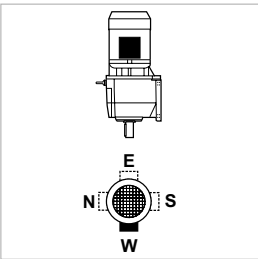
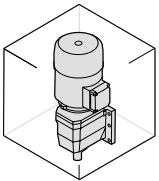


2x
3x

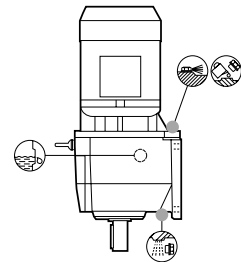
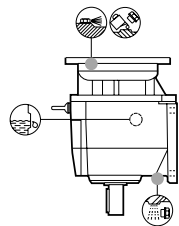
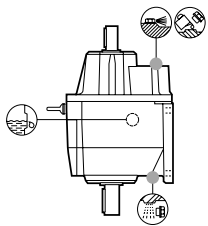


4x

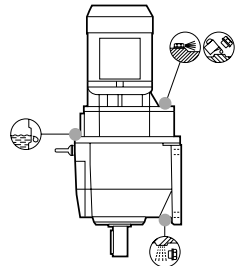
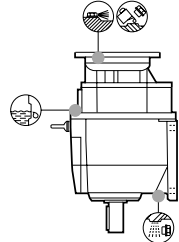
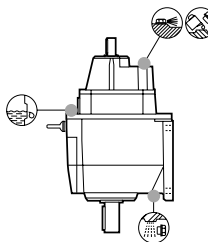
V5



W = Default

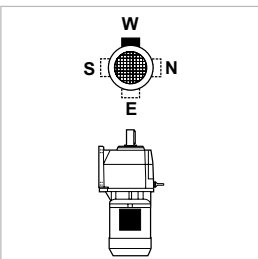
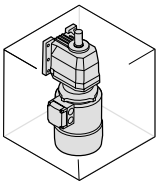


2x
3x

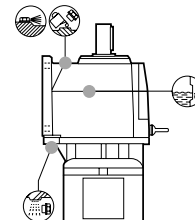
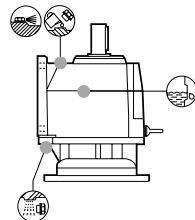
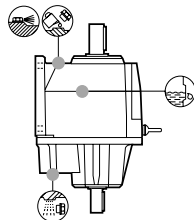


4x

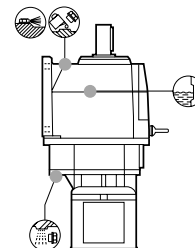
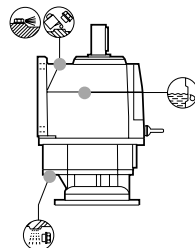
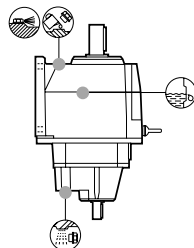
V6



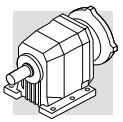
W = Default



2x
3x



4x



C 51...C 61

C_F

C_U

C_UF

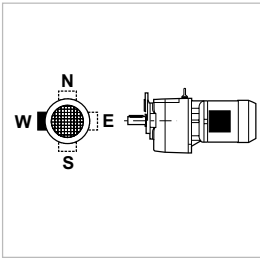
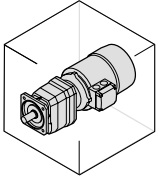
(B11)

_HS

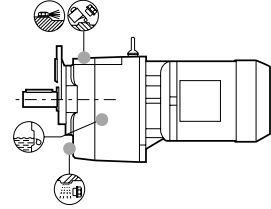
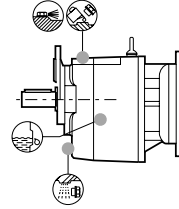
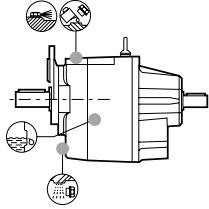
_P (IEC)

_S

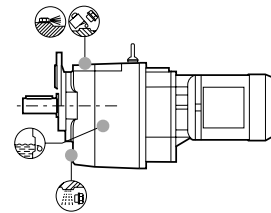
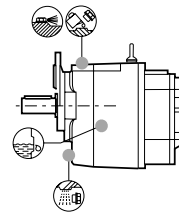
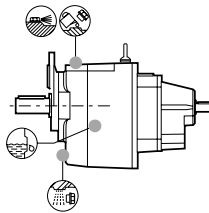
B5



W = Default

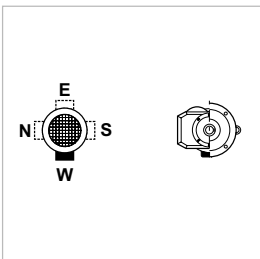
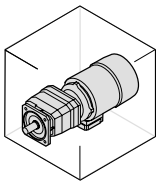


2x
3x

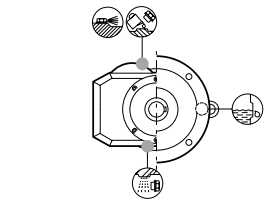
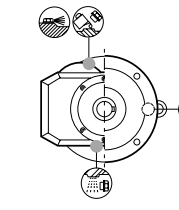
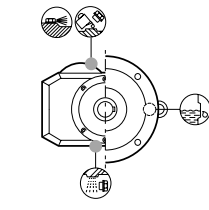


4x

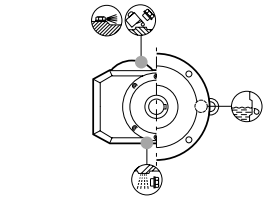
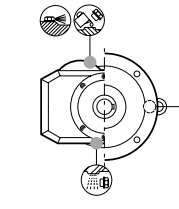
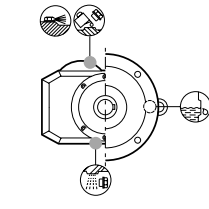
B51



W = Default

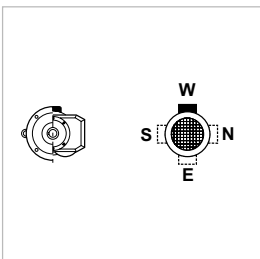
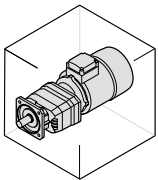


2x
3x

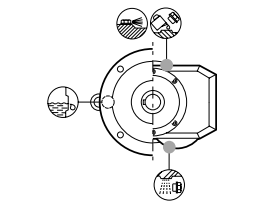
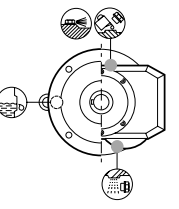
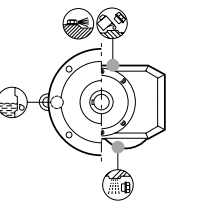


4x

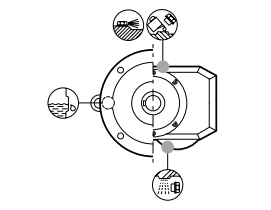
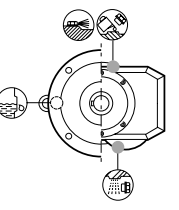
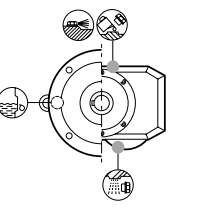
B53



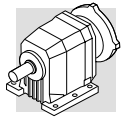
W = Default



2x
3x



4x



C_F

C_U

C_UF

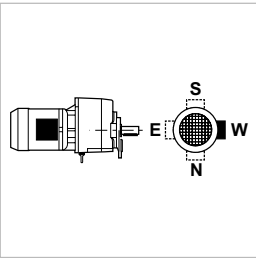
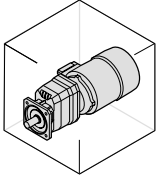
(B12)

_HS

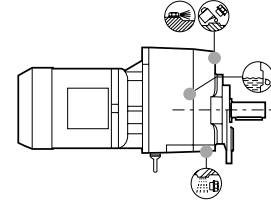
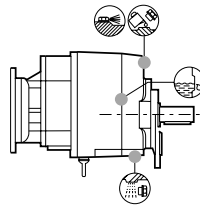
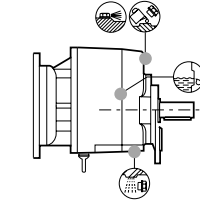
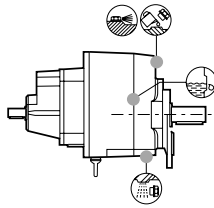
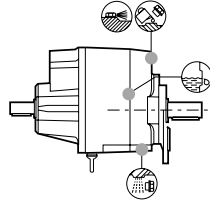
_P (IEC)

_S

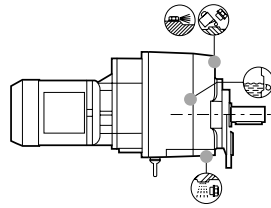
B52



W = Default

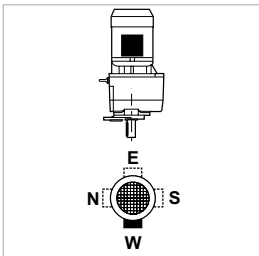
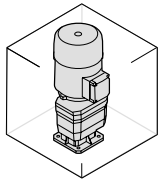


← 2x
3x

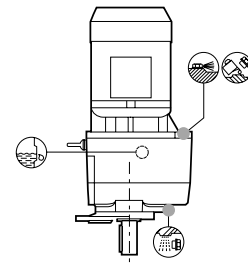
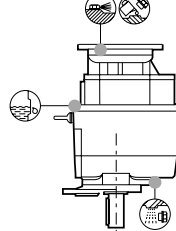
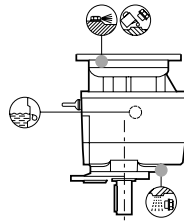
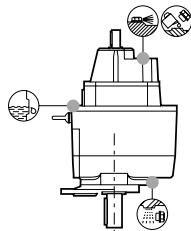
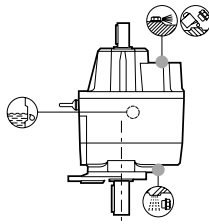


← 4x

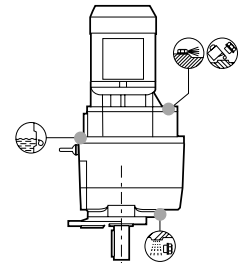
V1



W = Default

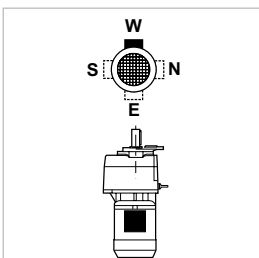
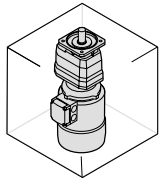


← 2x
3x

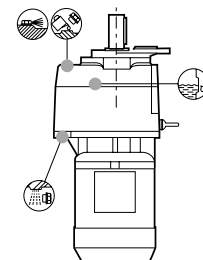
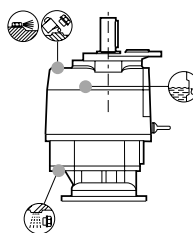
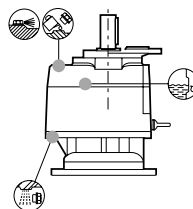
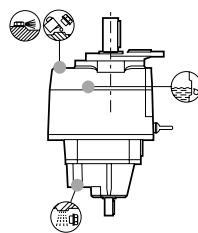
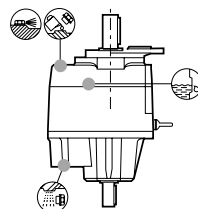


← 4x

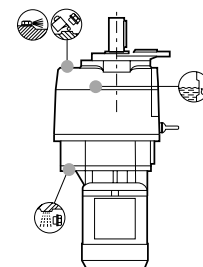
V3



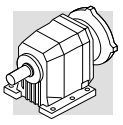
W = Default



← 2x
3x



← 4x



C 70...C 100

C_P

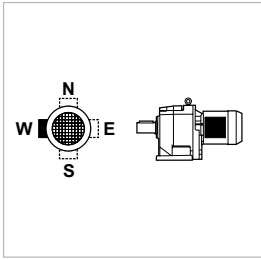
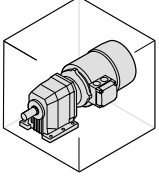
(B13)

_HS

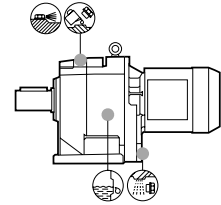
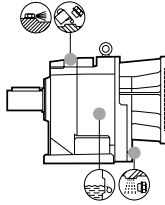
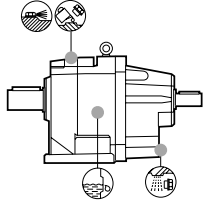
_P (IEC)

_S

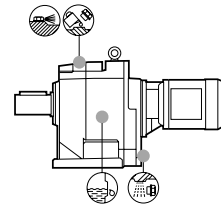
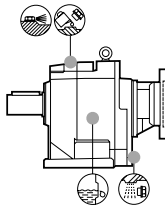
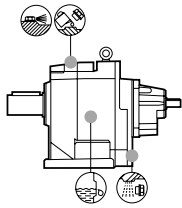
B3



W = Default

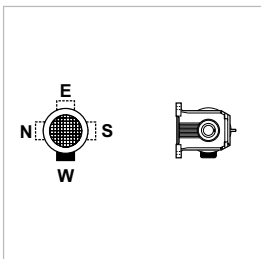
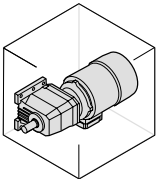


2x
3x

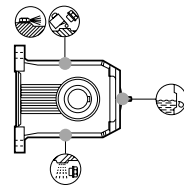
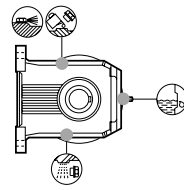
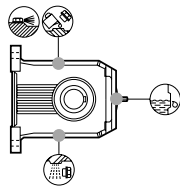


4x

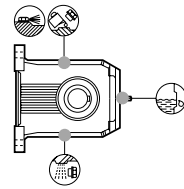
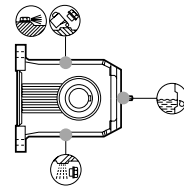
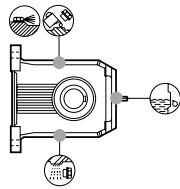
B6



W = Default

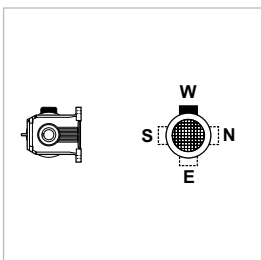
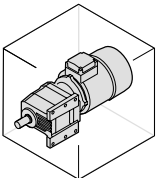


2x
3x

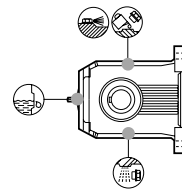
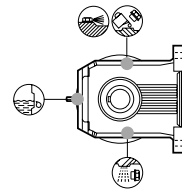
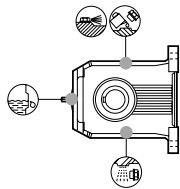


4x

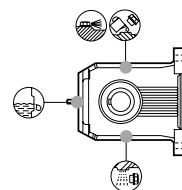
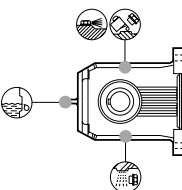
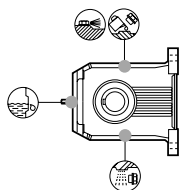
B7



W = Default



2x
3x



4x